



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IFW

Applicant(s): ITO et al.

Application No.: 10/760,489

Filed: January 21, 2004

Title: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE HAVING A
SAMPLING SIGNAL GENERATION
CIRCUIT

Attorney Docket No.: 01-543

Group Art Unit: 2816

Examiner: COX, CASSANDRA F

Commissioner for Patents
Alexandria, VA 22314

May 3, 2005

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to a Restriction Requirement mailed on April 5, 2005 in connection with the above application, Applicants hereby elect Group I (claims 1 – 9, drawn to a semiconductor integrated circuit) without traverse.

Examination of the present application in view of the above election is respectfully requested.

Please charge any necessary fees to Deposit Account 50-1147.

Respectfully submitted,

David G. Posz
Reg. No. 37,701

DGP/yfm
Posz Law Group, PLC
12040 South Lakes Drive
Suite 101
Reston, VA 20191
Phone: (703) 707-9110
Customer No. 23400